

Substitute for form 1449A/PTO

Compleat if Known.

Application Number	10/085,440
Filing Date	February 28, 2002
First Named Inventor	Axel Tillman et al
Art Unit	2154
Examiner Name	TBA
Attorney Docket Number	111460.125WO1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 9 of 1

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	V. Vw	Date Considered	10-21-03
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INFORMATION DISCLOSURE
IN AN APPLICATION

(Use several sheets if necessary)

Applicant

Tillmann et al

Filing Date

February 28, 2002

Group Art Unit

Not Yet Assigned

Sheet 1 OF 1

U.S. Patent Documents

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
✓✓	5,481,601	01/1996	Nazif et al	379	207	
✓✓	5,680,585	10/1997	Bruell	395	500	
✓✓	6,081,212	06/2000	Tagato et al	341	79	
✓✓	6,085,240	07/2000	Suzuki et al	709	223	
✓✓	6,138,154	10/2000	Karino	709	223	
✓✓	6,138,272	10/2000	Tonouchi	717	5	

Foreign Patent Documents

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

✓✓		Srivastava, M., et al., "Using VHDL for High Level, Mixed-Mode System Simulation," IEEE Design & Test of Computers, IEEE, pp. 31-40.
✓✓		Newton, H., Newton's Telecom Dictionary, Telecom Books, New York, NY, p. 605.

EXAMINER

J. ✓✓

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